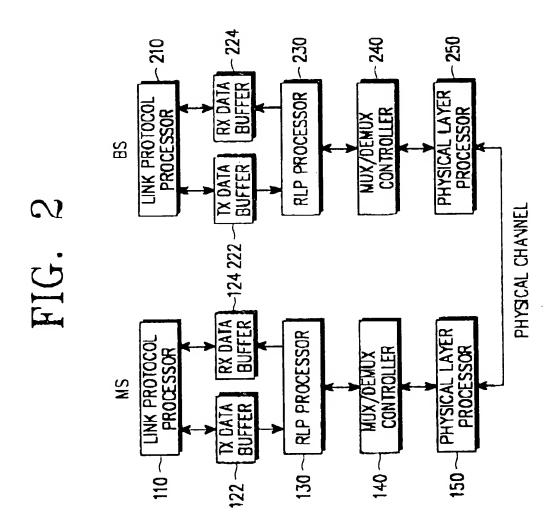


FIG.



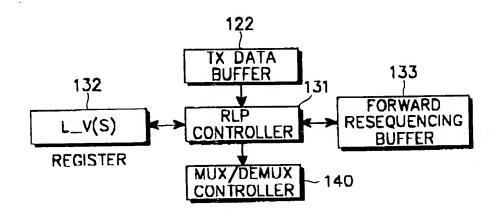


FIG. 3

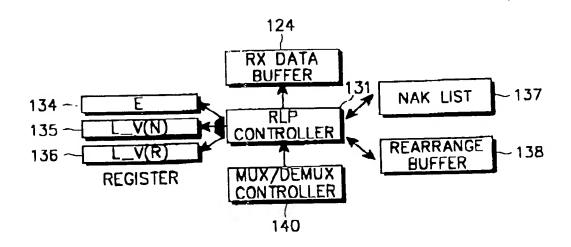


FIG. 4

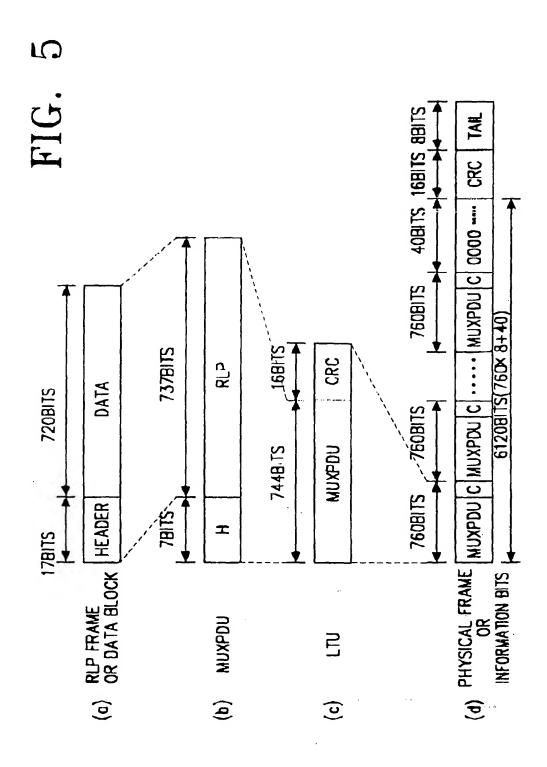
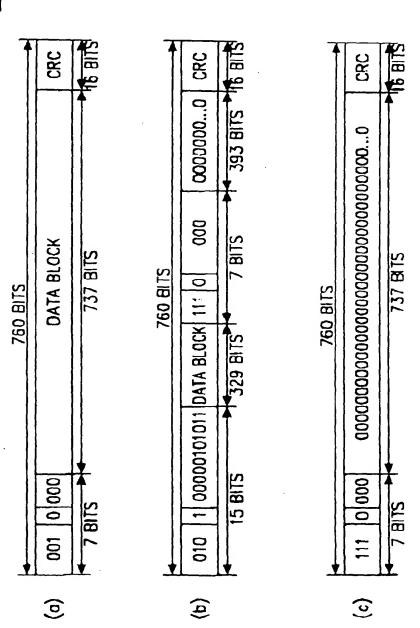


FIG. 6



TYPE A DATA BLOCK FUNDAMENTAL LOW RATE FRAME N RITS

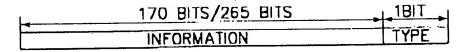
1	N BITS
	WEODWA TION
	INFORMATION

TYPE B DATA BLOCK SUPPLEMENTAL FRAME

1 BIT	(N*8) BITS
TYPE	INFORMATION

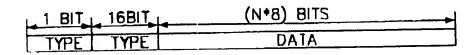
TYPE = '0'

FUNDAMENTAL RATE 1 FRAME



TYPE = '0'

TYPE C DATA BLOCK SUPPLEMENTAL DATA FRAME



TYPE = '1'

FUNDAMENTAL RATE 1 DATA FRAME

16 BITS	152 BITS/248 BITS	2 BITS/1 BIT 1 BIT
SEQ	DATA	PADDING TYPE

TYPE = '1' ...

FIG. 7

INFORMATION EXCEPT 1/8 RATE

	• • • •			
	=== 4145		CDAME	PADDING
	FRAME	,	LIVANE	T ACCION
I TRAME	111711110			

ONE CONTROL FRAME, OR AT MOST ONE DATA FRAME AND RETRANSMITTED FRAMES

IDLE FRAME(1/8 .RATE ONLY)

16 BITS | SEQ | PADDING

SYNC, SYNC/ACK, ACK FRAME

CTL - '11100001' FOR SYNC, '11100010' FOR SYNC/ACK, '11100011' FOR ACK

VARIABLE-LENGTH DATA FRAME

3 BITS	5 BITS	16 BITS	(LEN+8) BITS
CTL	LEN	SEQ	DATA

CTL = '001'

3 BITS	13 BITS	16 BITS	(LEN*B) BITS
CTL	LEN	SEQ	DATA

CTL = '010'

•	JIL - U	, -
3 BITS	21 RITS	(LEN®) BITS
2 DIIS	2. 0	
CTI	SFO	DAIA
	<u> </u>	

CTL = '100'

3 BITS.	21 BITS	B BITS	(LEN*B) BITS
CTL	SEQ	LEN	DATA

CTL = '101'

3 BITS	21 BITS	16 BITS	(LENº8) BITS
CTL	SEQ	LEN	DAŢA

CTL = '110'

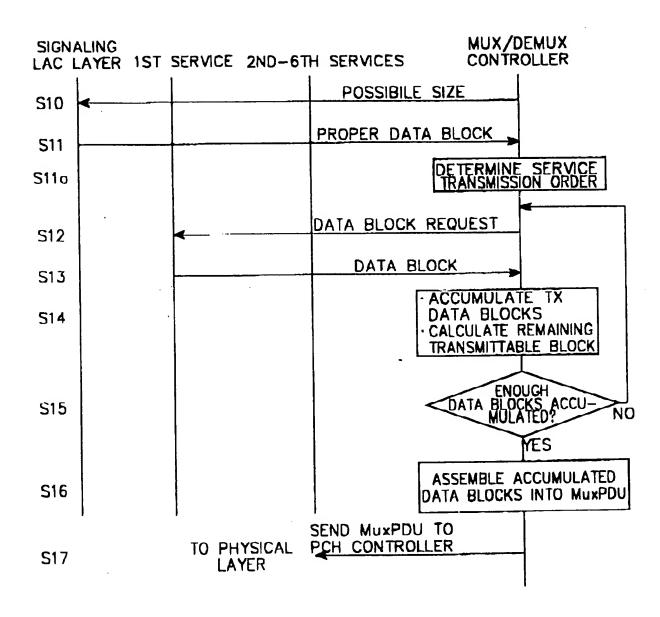


FIG. 9

FCH RECEIVING

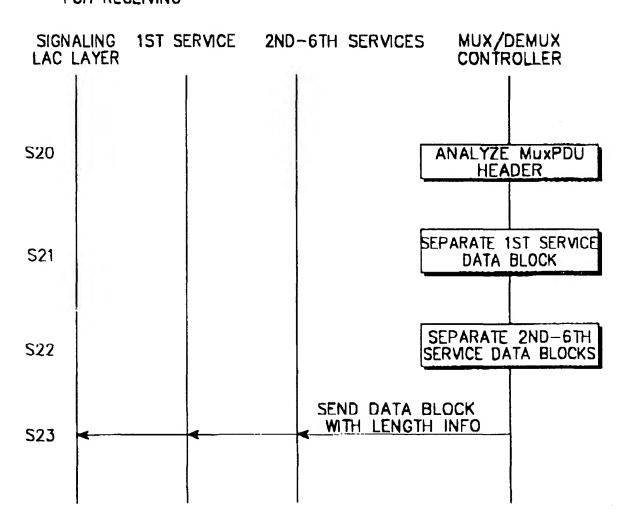


FIG. 10

SCH TRANSMISSION

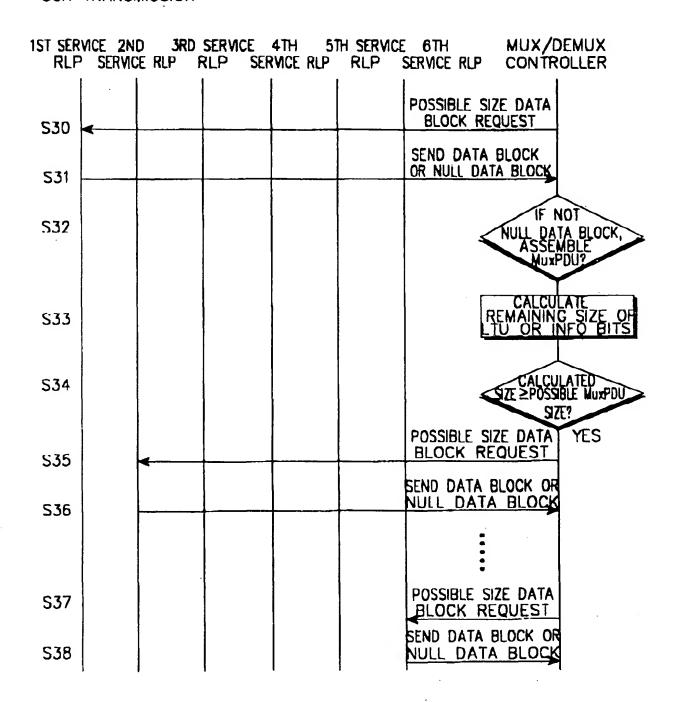


FIG. 11

SCH RECEIVING

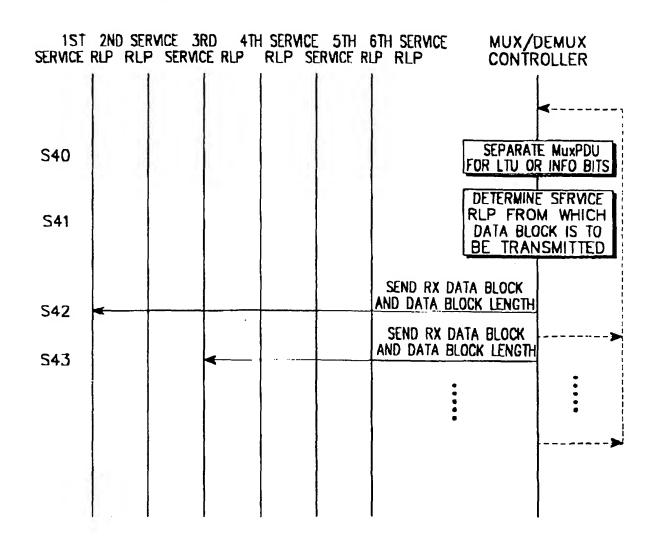


FIG. 12